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PATENT APPLICATION

**RESPONSE UNDER 37 CFR §1.116
EXPEDITED PROCEDURE
TECHNOLOGY CENTER ART UNIT 2822**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Koji YAMAGUCHI

Group Art Unit: 2822

Application No.: 10/753,357

Examiner: R. POTTER

Filed: January 9, 2004

Docket No.: 118292

For: WIRING SUBSTRATE, SEMICONDUCTOR DEVICE, SEMICONDUCTOR
MODULE, ELECTRONIC EQUIPMENT, METHOD FOR DESIGNING WIRING
SUBSTRATE, METHOD FOR MANUFACTURING SEMICONDUCTOR DEVICE,
AND METHOD FOR MANUFACTURING SEMICONDUCTOR MODULE

AMENDMENT AFTER FINAL REJECTION UNDER 37 CFR §1.116

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In reply to the January 25, 2006 Office Action, please consider the following:

Amendments to the Claims as reflected in the listing of claims;

Remarks.